Date Mailed; February 24, 2004

Sheet 1 of 1

FORM 1449* INFORMATION DISCLOSURE STATEMENT IN AN APPLICATION (Use several sheets if necessary)	Docket Number: 10873.1358US01	Application Number: 10/730,839
	Applicant: OZASA, et al.	
	Filing Date: December 8, 2003	Group Art Unit: ~~Unknown~~ 2816

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	2-124609	May, 1990	Japan	—	—	abstract	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		"Semiconductor Circuit Design Technology", Nikkei Business Publishers Inc., edited by T. Tamai, 1st edition, p. 302
		"CMOS Analog Circuit Design Second Edition", p. 196, published by OXFORD, Phillip E. Allen and Douglas R. Holberg

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DATE CONSIDERED 3/3/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.

*Substitute Disclosure Statement Form (PTO-1449)